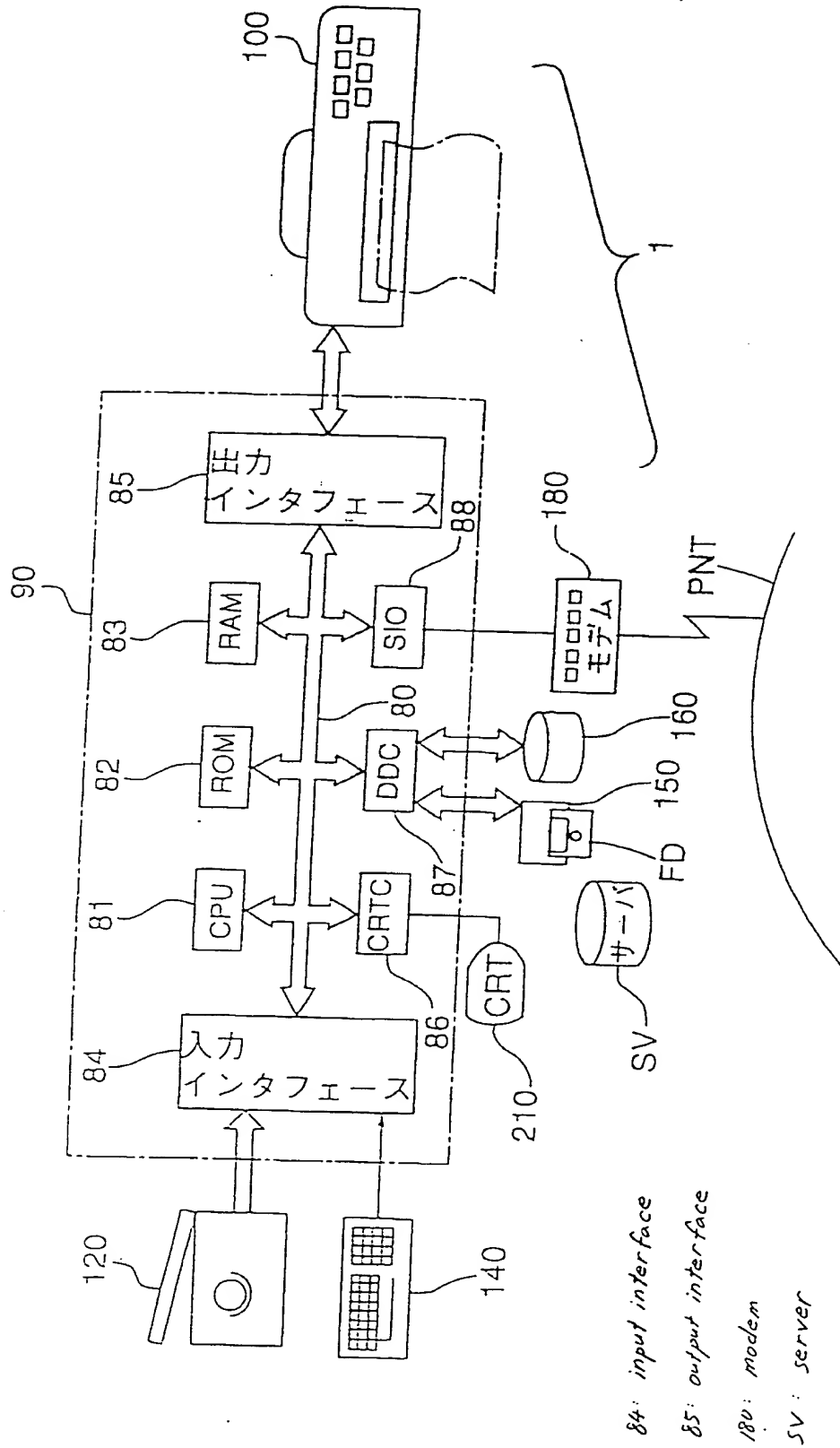
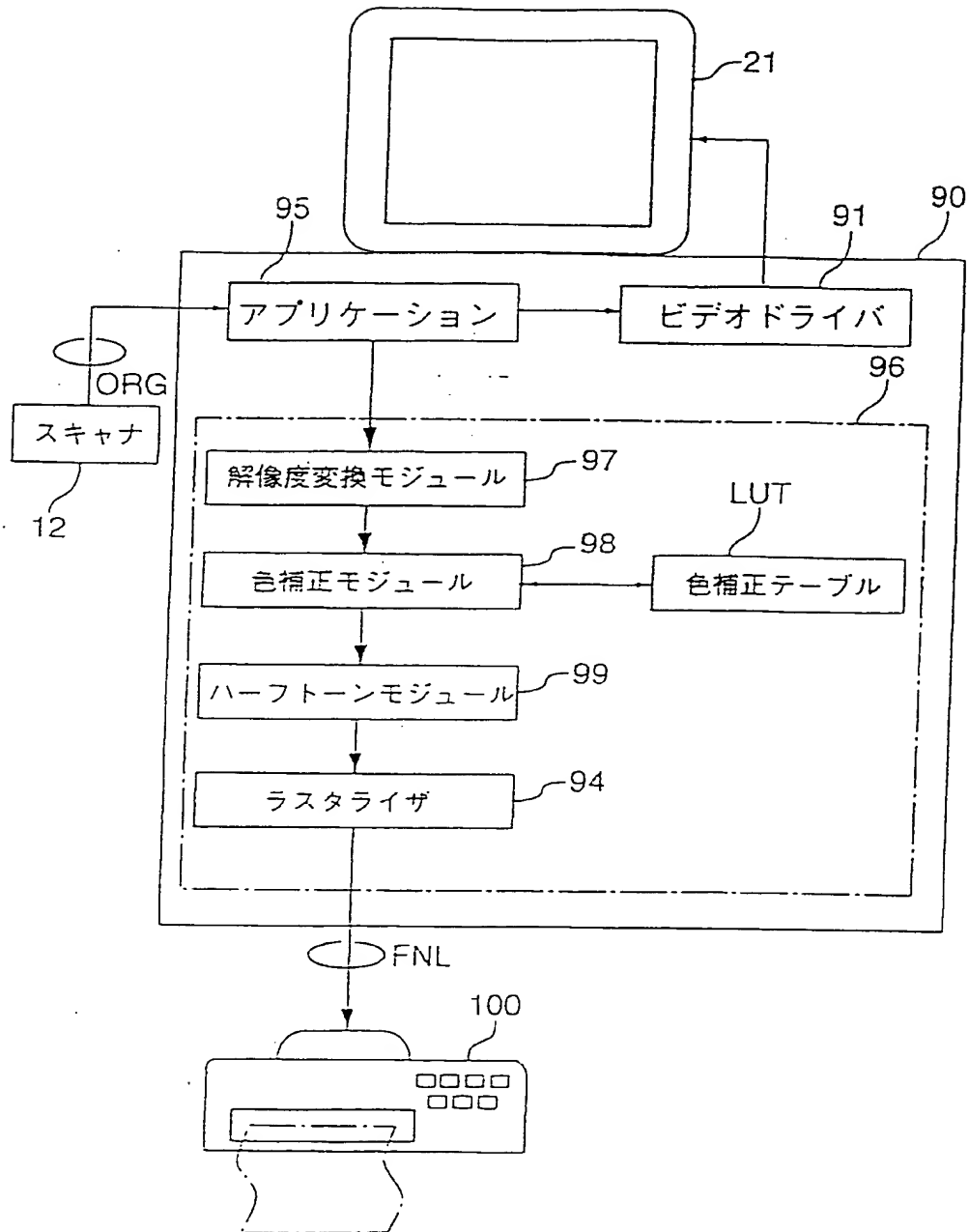


Fig. 1



84: input interface
 85: output interface
 180: modem
 SV: server

Fig. 2



- 12: scanner
- 91: video driver
- 94: rasterizer
- 95: application
- 97: resolution conversion module
- 98: color correction module
- 99: halftone module

Fig. 3

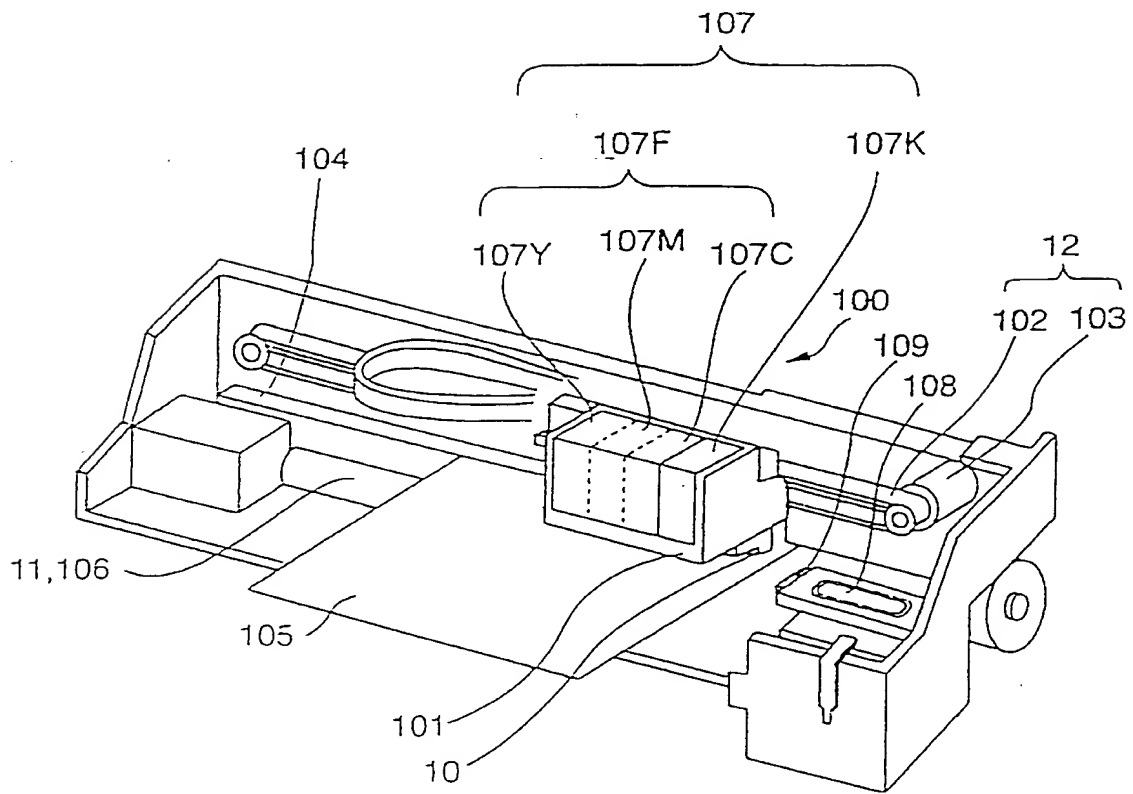


FIG. 3

Fig. 4

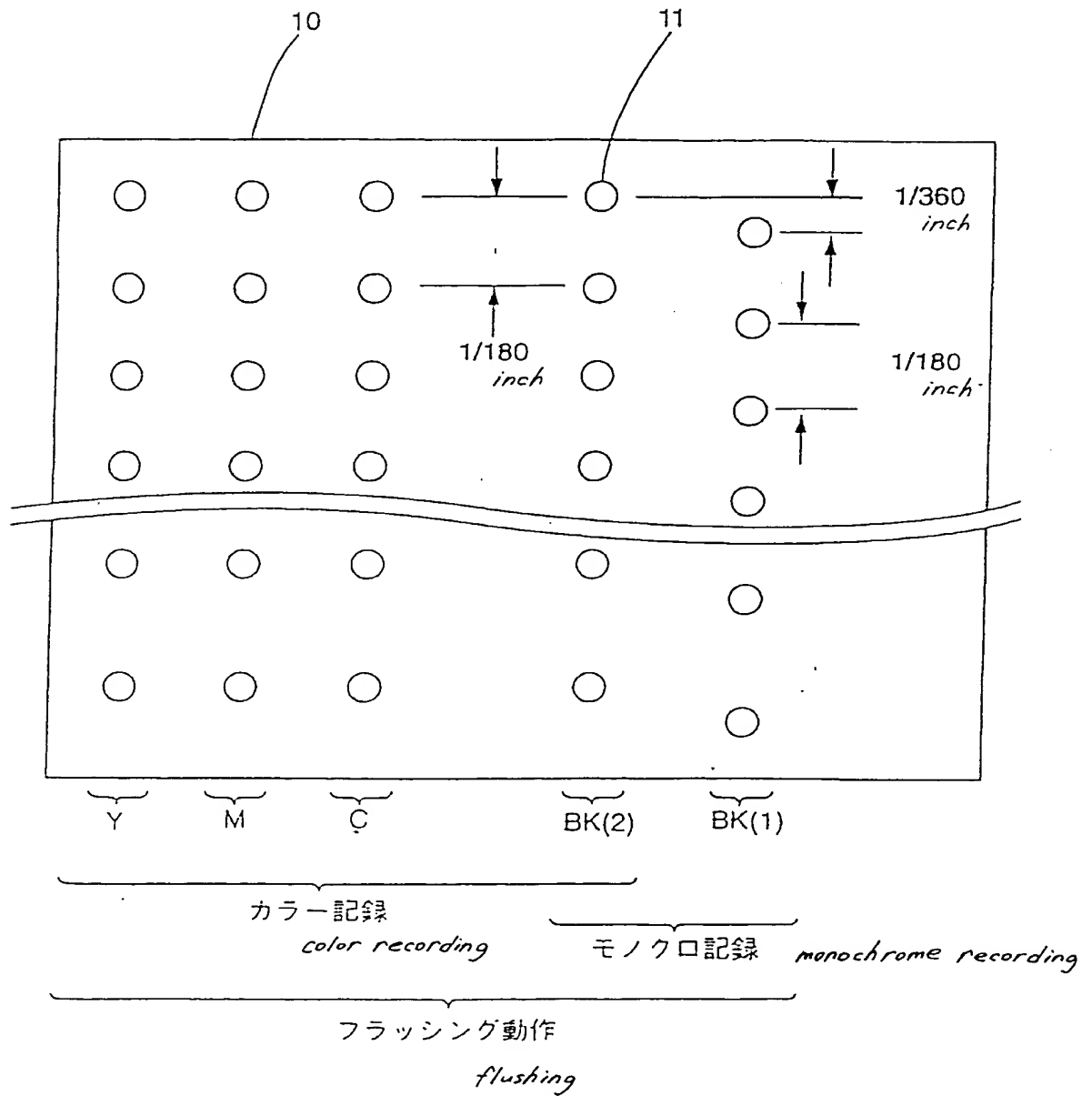
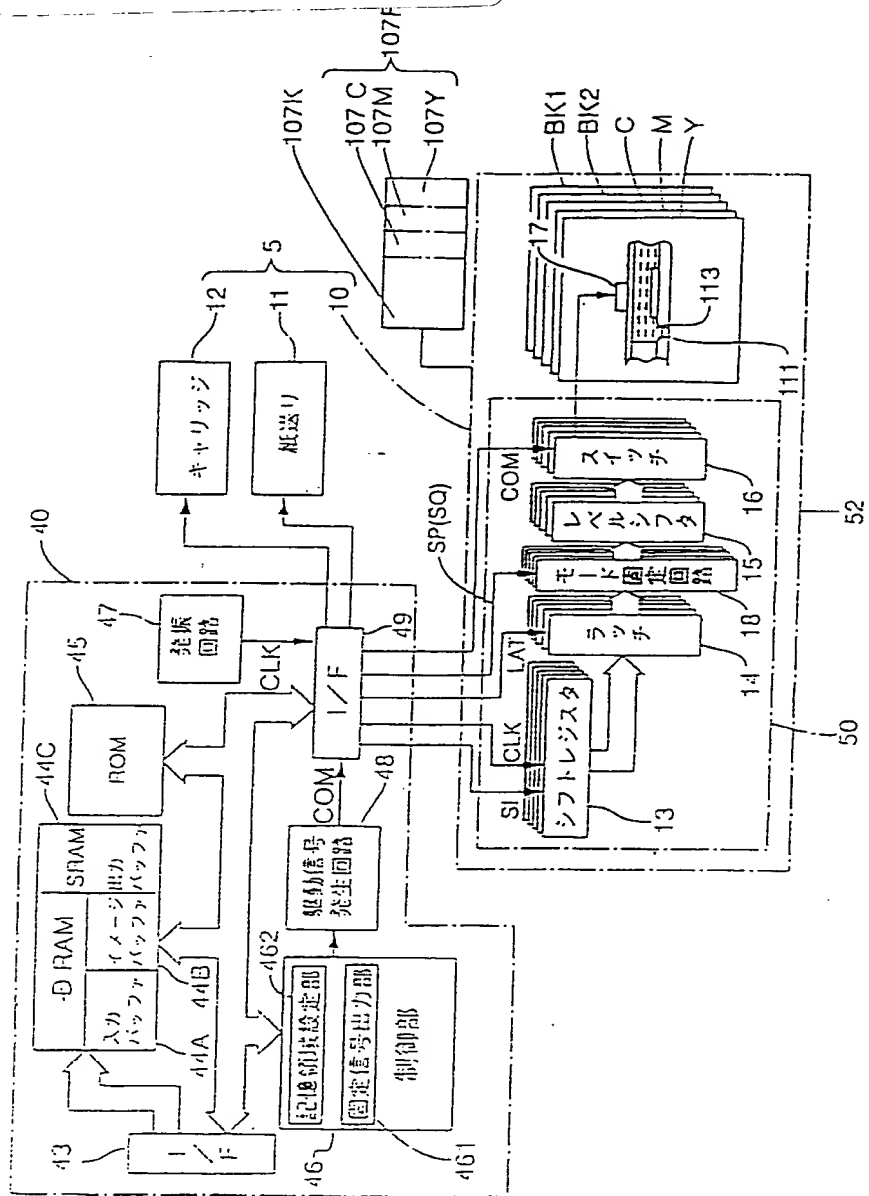


Fig. 5



- | | |
|------|--------------------------------|
| 11: | paper transport |
| 12: | carriage |
| 13: | shift register |
| 14: | latch |
| 15: | level shifter |
| 16: | switch |
| 18: | mode fixing circuit |
| 44A: | input buffer |
| 44B: | image buffer |
| 44C: | output buffer |
| 46: | control section |
| 461: | fixing signal output section |
| 462: | storage region setting section |
| 47: | oscillation circuit |

Fig. 6

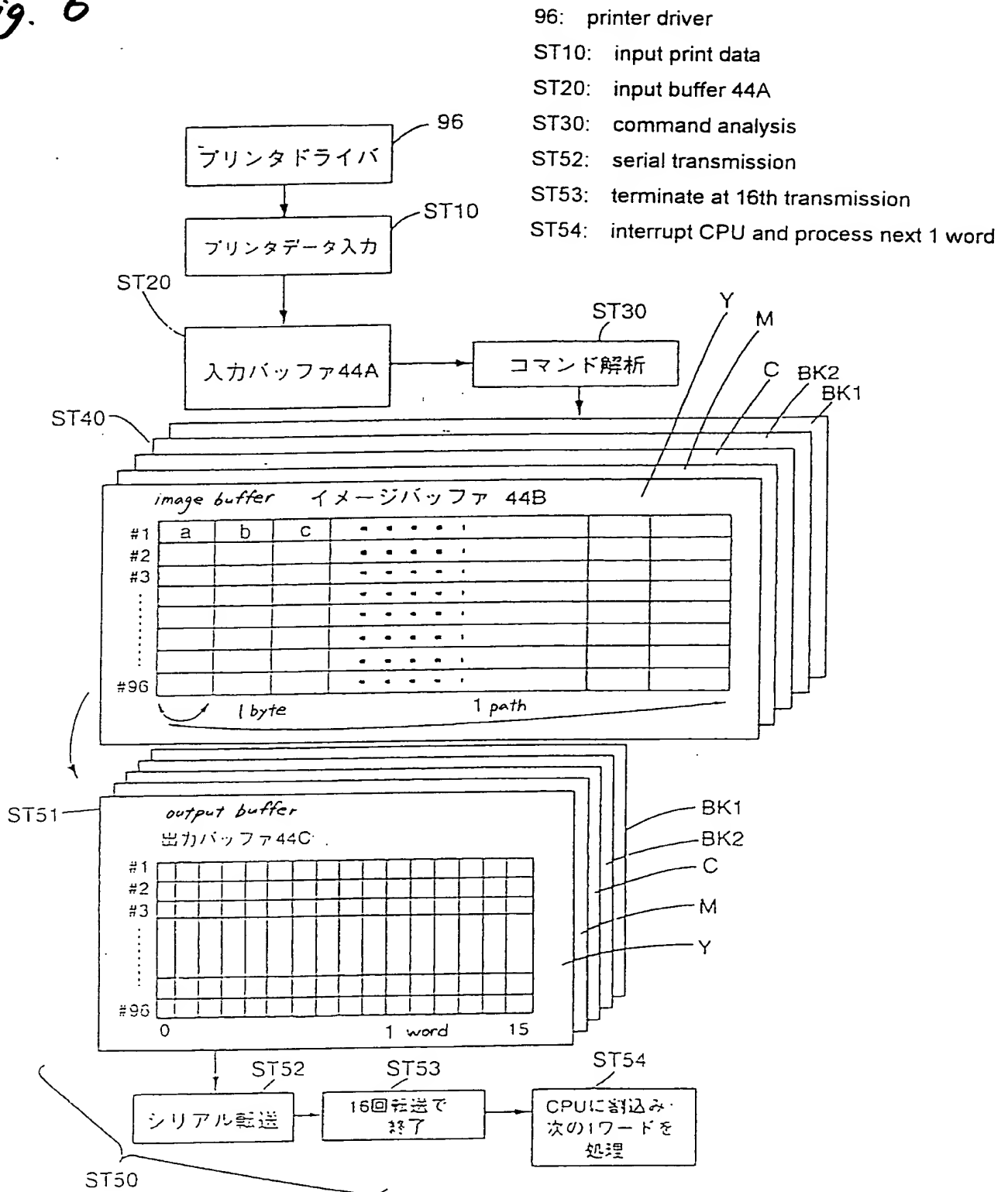
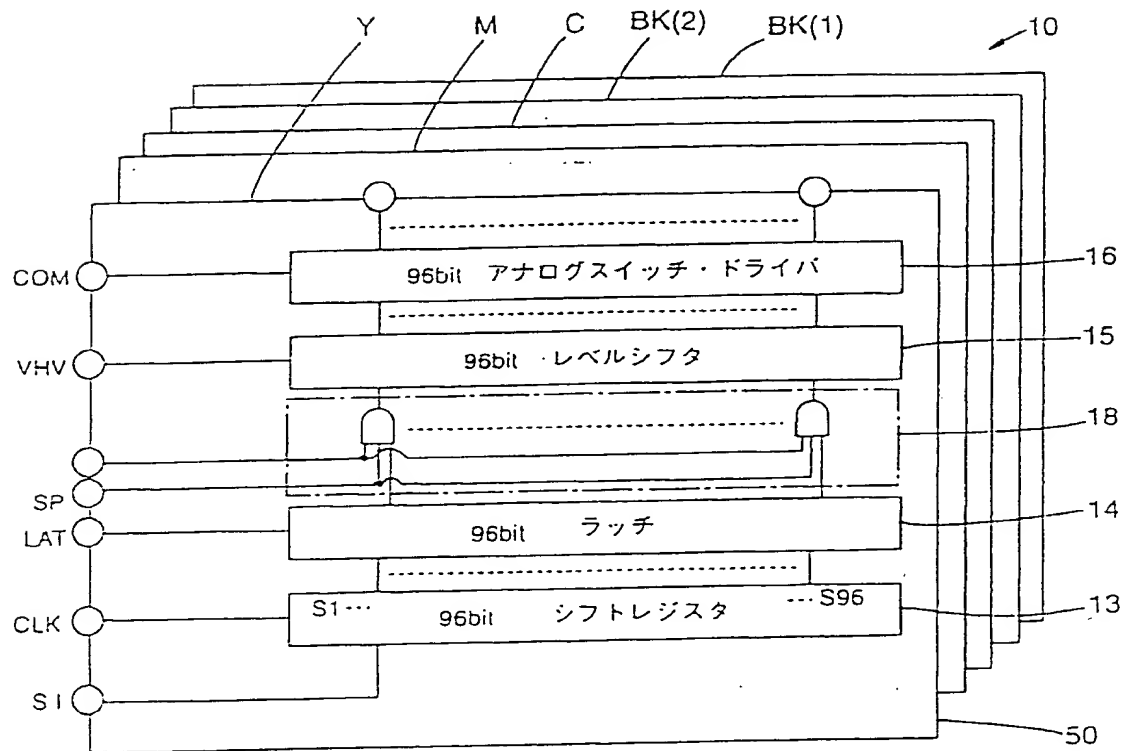
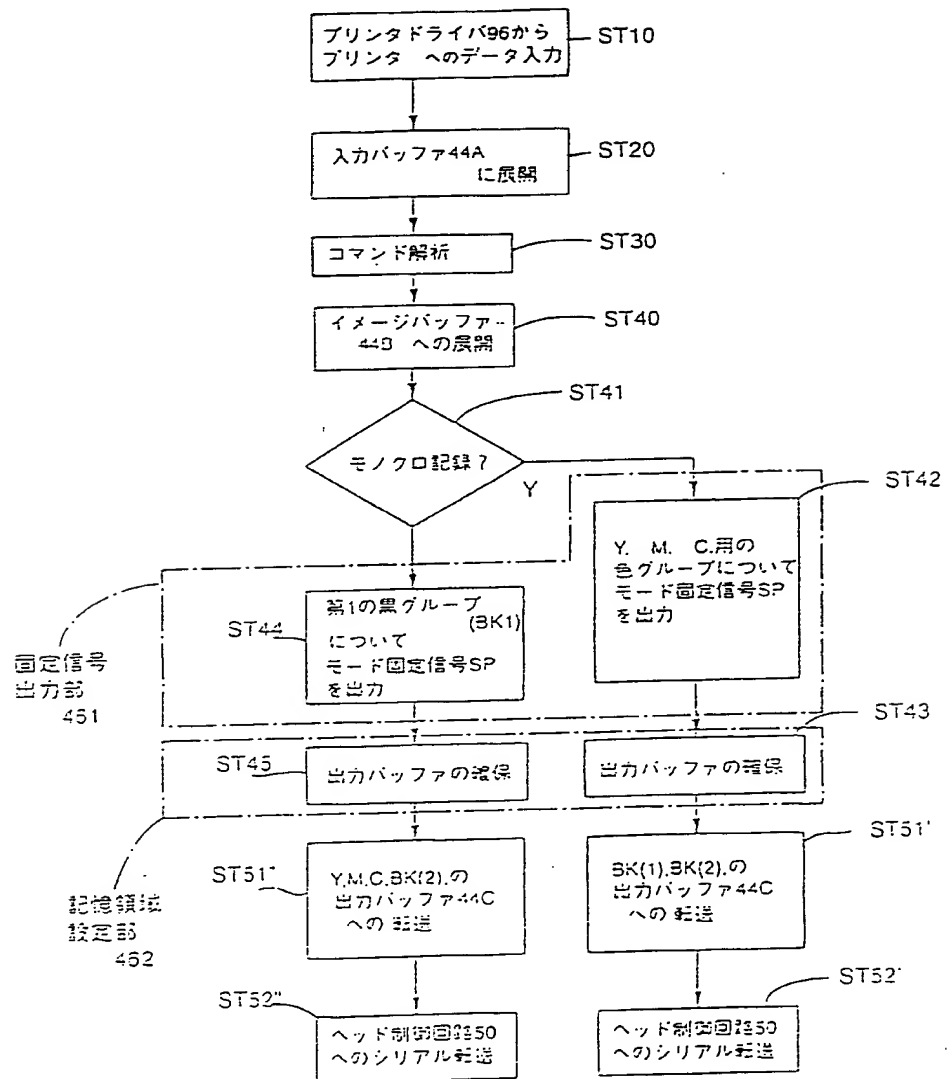


Fig. 7



- 13: shift register
- 14: latch
- 15: level shifter
- 16: analog switch driver

Fig. 8



- ST10: data input from printer driver 96 to printer
 ST20: data expansion in input buffer 44A
 ST30: command analysis
 ST40: data expansion in image buffer 44B
 ST41: monochrome recording?
 ST42: output mode fixing signal SP to color group (Y, M, C)
 ST43: reserve output buffer
 ST44: output mode fixing signal SP to first black group (BK1)
 ST45: reserve output buffer
 ST51': transfer BK(1) and BK(2) to output buffer 44C
 ST52': serial transmission to head driver circuit 50
 ST51'': transfer Y, M, C and BK(2) to output buffer 44C
 ST52'': serial transmission to head driver circuit 50

Fig. 9

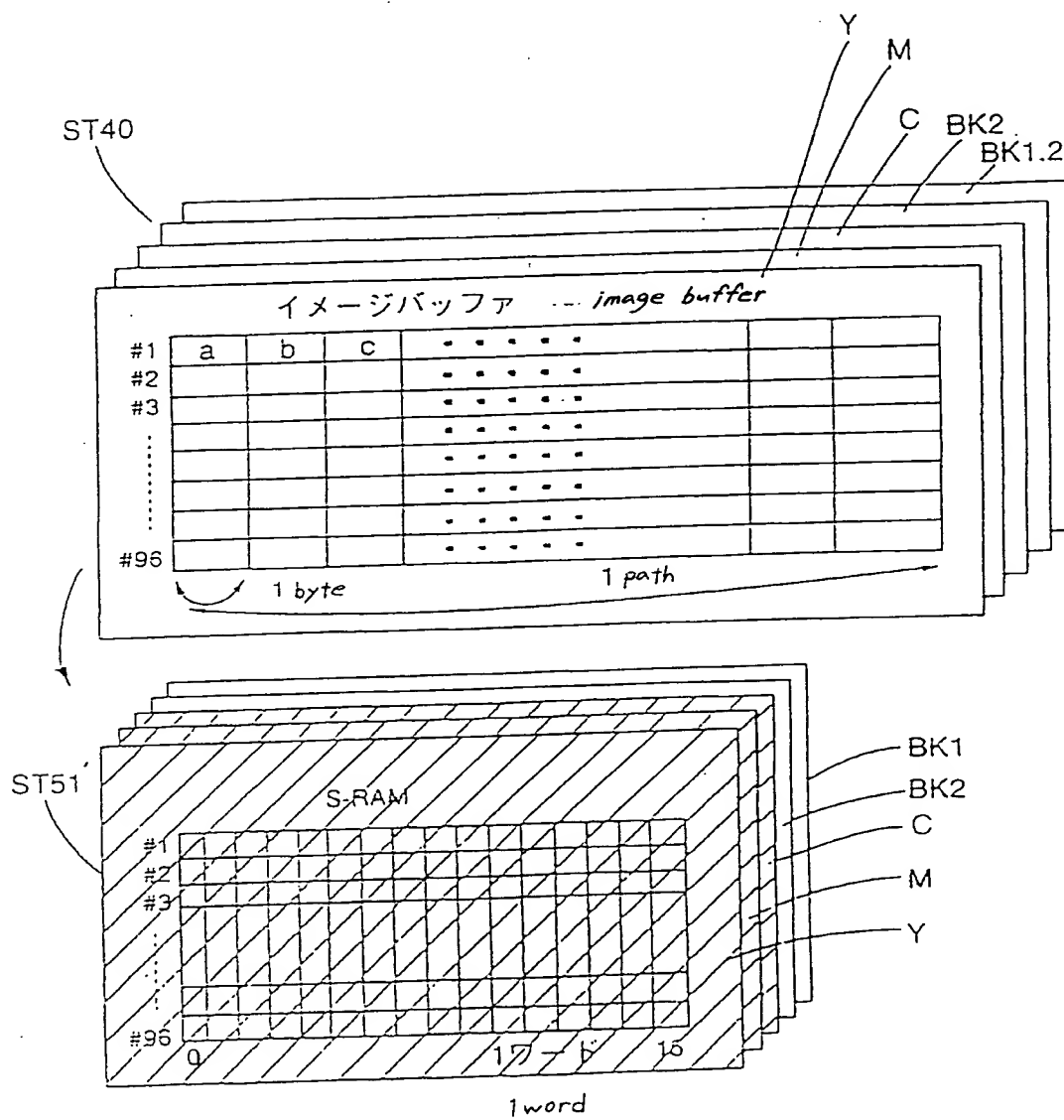


Fig. 10

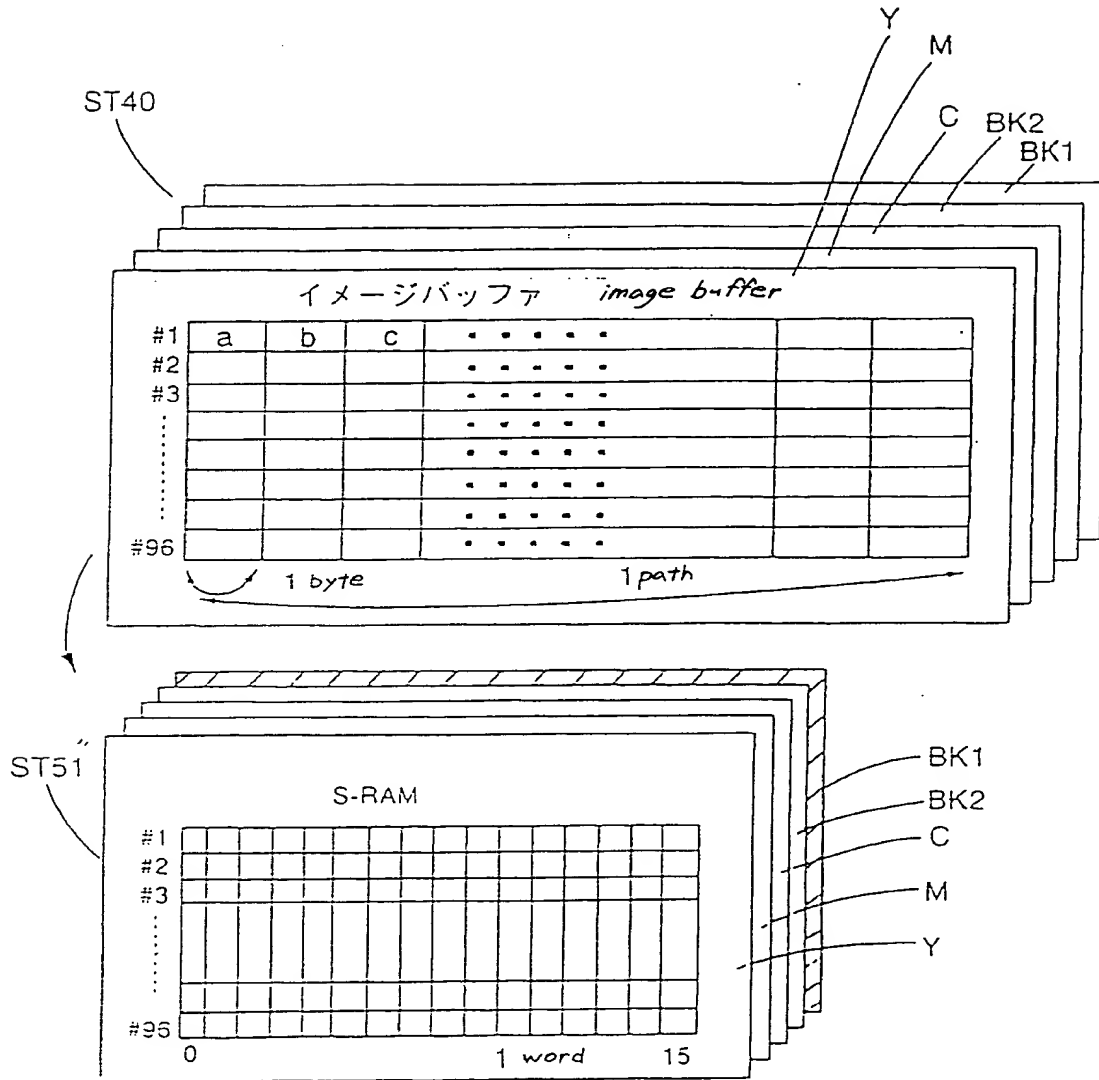


Fig. 11

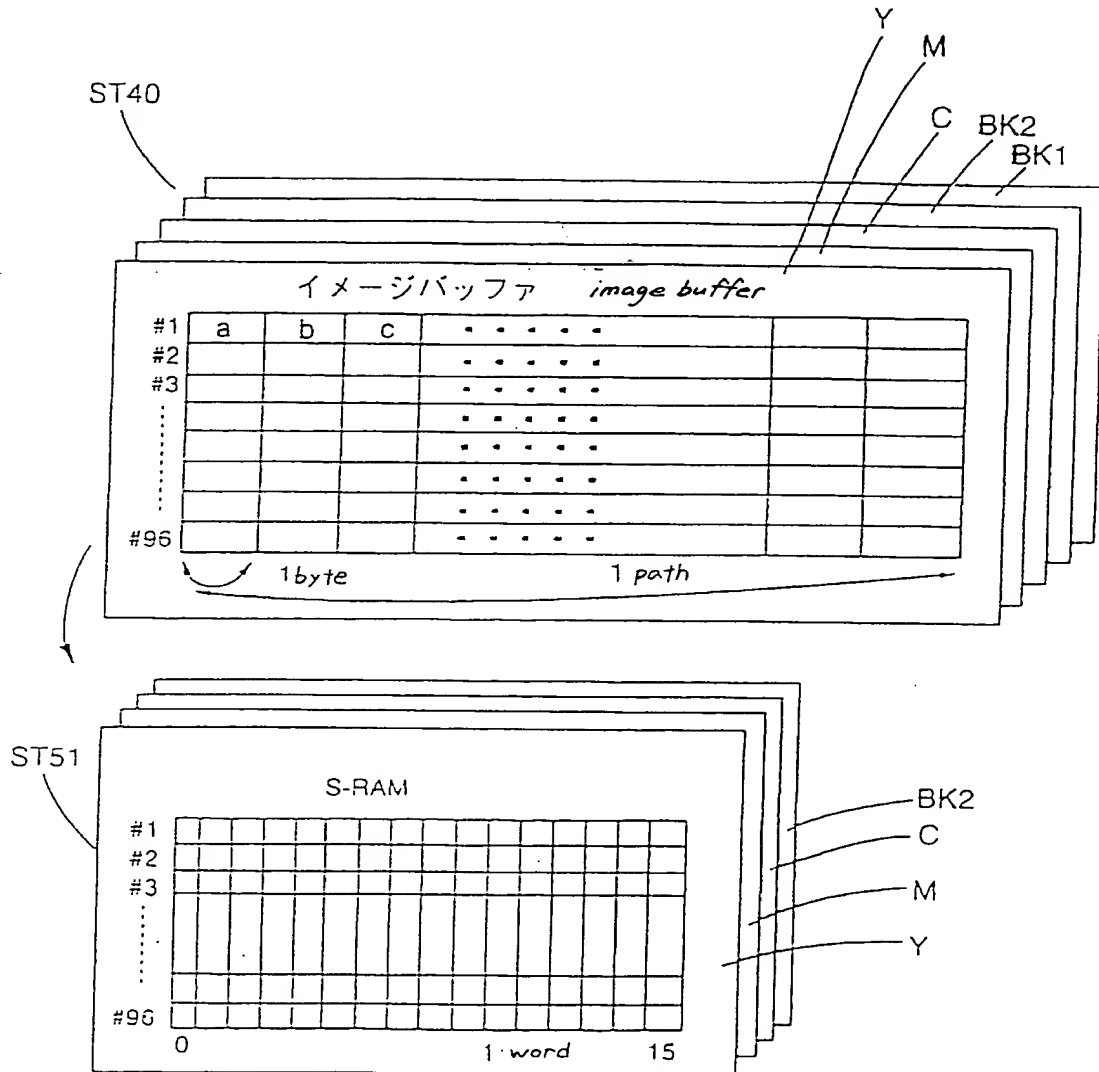


Fig. 12

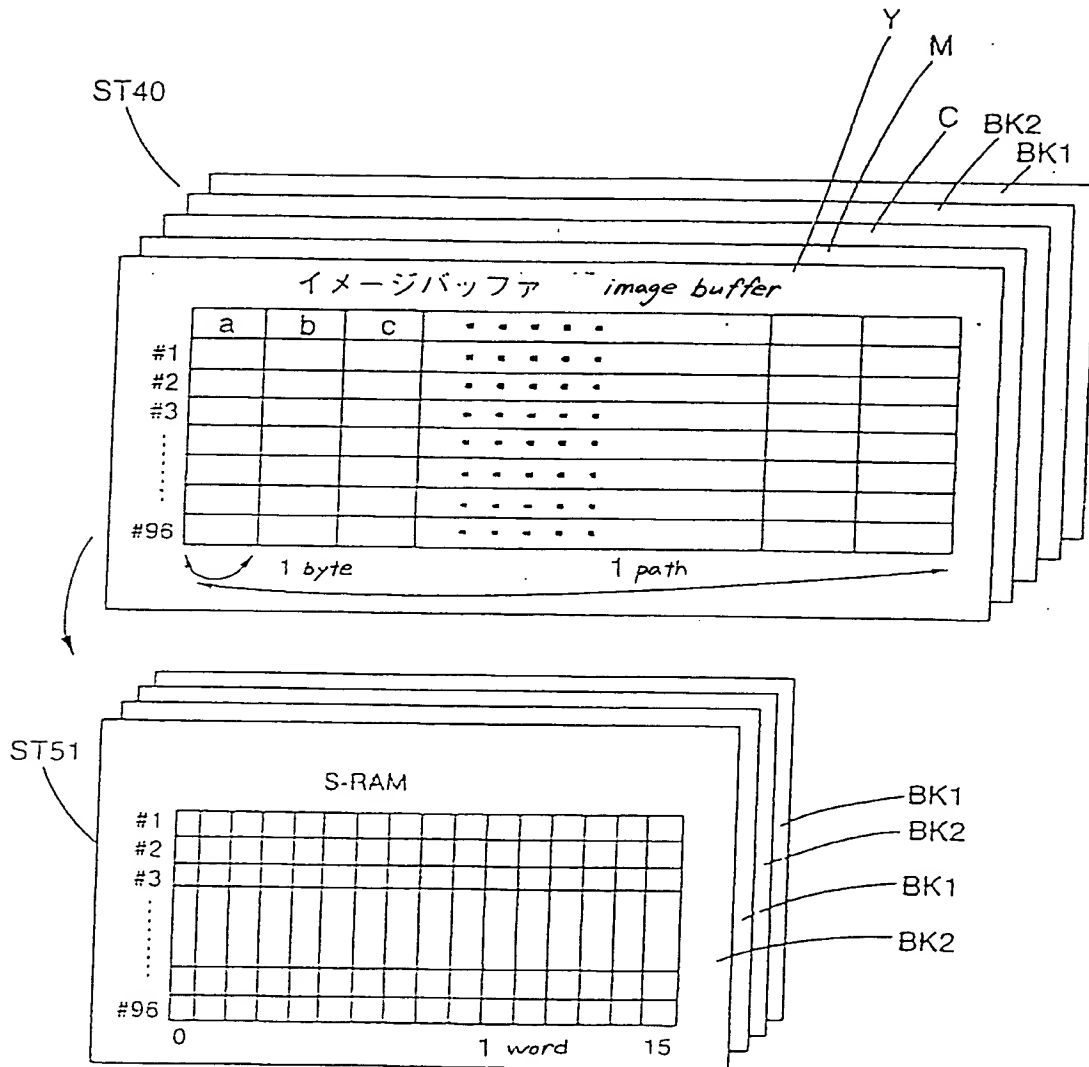
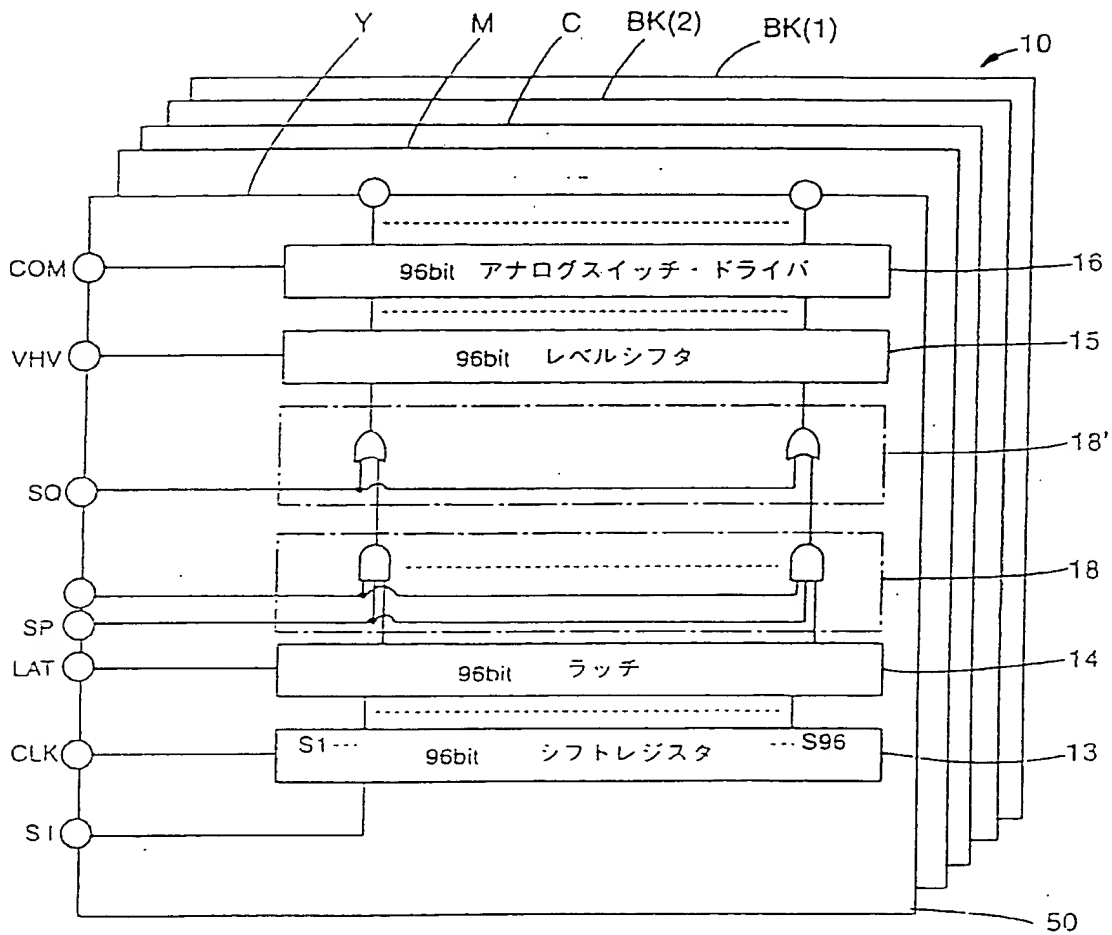


Fig. 13



- 13: shift register
- 14: latch
- 15: level shifter
- 16: analog switch driver